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Attorney's Docket No.: 42P15685

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Scott A. Hareland et al.

Application No.: 10/607,769

Filed: June 27, 2002

For: NONPLANAR SEMICONDUCTOR
DEVICE WITH PARTIALLY OR FULLY
WRAPPED AROUND GATE ELECTRODE
AND METHODS OF FABRICATION

Examiner: Unassigned

Art Unit: Unassigned

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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(Date of Deposit)

Teresa Mattox

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Teresa Mattox

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/21, 2004


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STATEMENT BY APPLICANT

Sheet

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of

Application Number

10/607,769

Filing Date

June 27, 2003

First Named Inventor:

Scott A. Hareland

Art Unit

Unassigned

Examiner Name

Unassigned

Attorney Docket Number

42P15685

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner
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